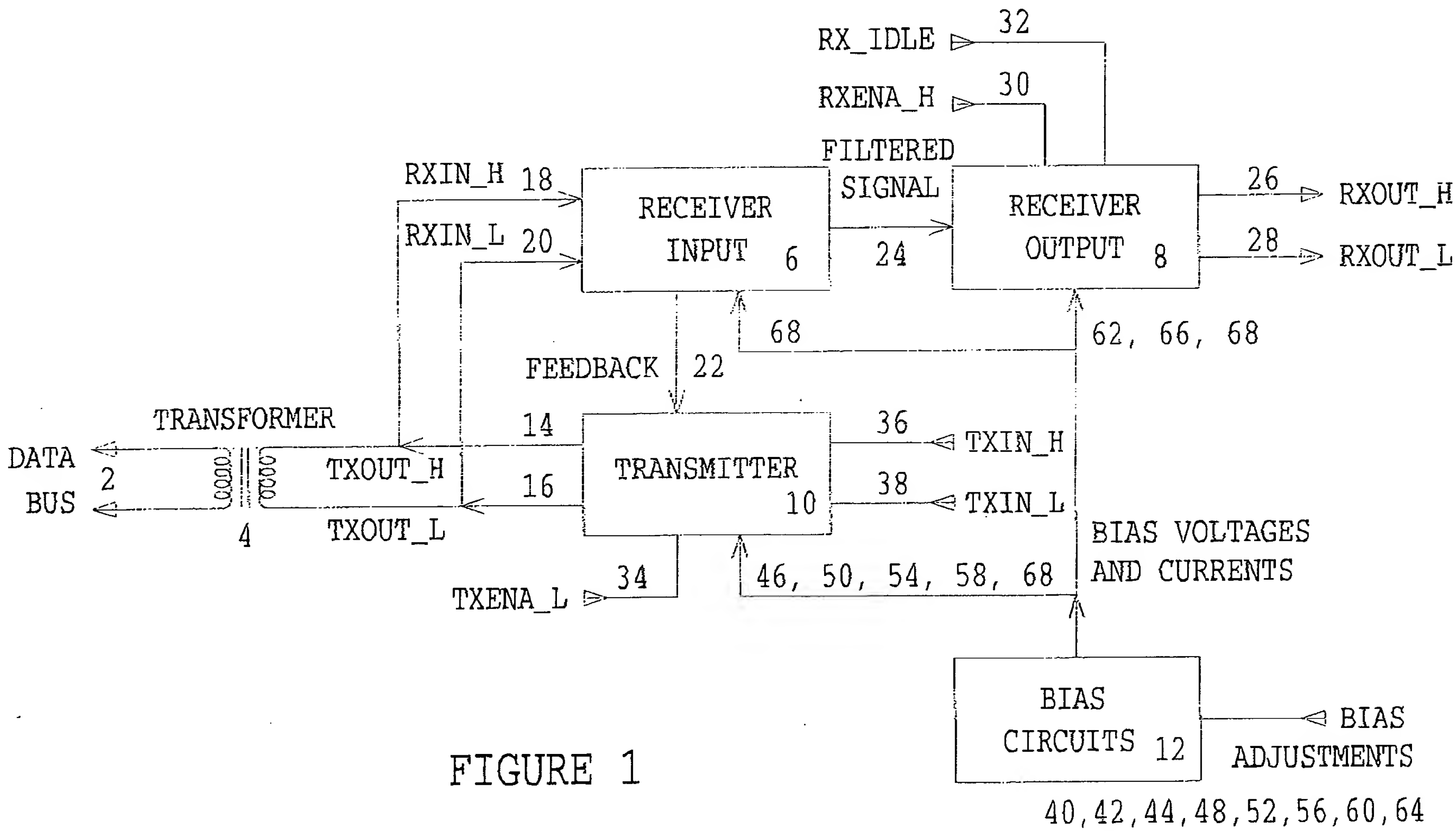
RX_IDLE
32
RXENA_H
30
FILTERED
SIGNAL
RXIN_H
18
RXIN_L
20
RECEIVER
INPUT
6
24
RECEIVER
OUTPUT
8
26
RXOUT_H
28
RXOUT_L
68
62, 66, 68
FEEDBACK
22
TRANSFORMER
DATA
BUS
2
4
14
TXOUT_H
16
TXOUT_L
TRANSMITTER
10
36
TXIN_H
38
TXIN_L
BIAS VOLTAGES
AND CURRENTS
34
TXENA_L
46, 50, 54, 58, 68
BIAS
CIRCUITS
12
BIAS
ADJUSTMENTS
40,42,44,48,52,56,60,64

FIGURE 1

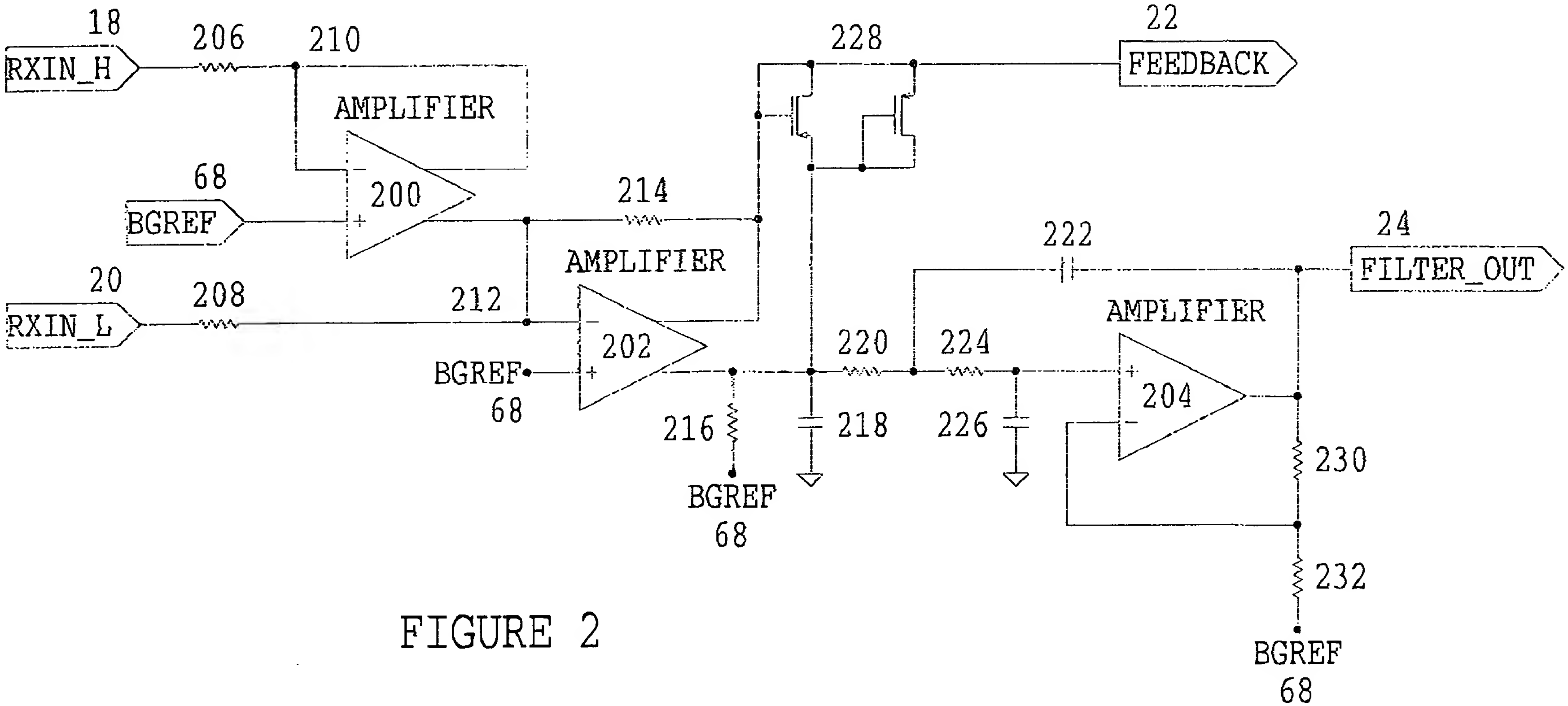
18
RXIN_H
206
210
AMPLIFIER
68
BGREF
200
214
20
RXIN_L
208
212
AMPLIFIER
202
BGREF
68
216
BGREF
68
228
22
FEEDBACK
222
24
FILTER_OUT
220
224
218
226
AMPLIFIER
204
230
232
BGREF
68

FIGURE 2

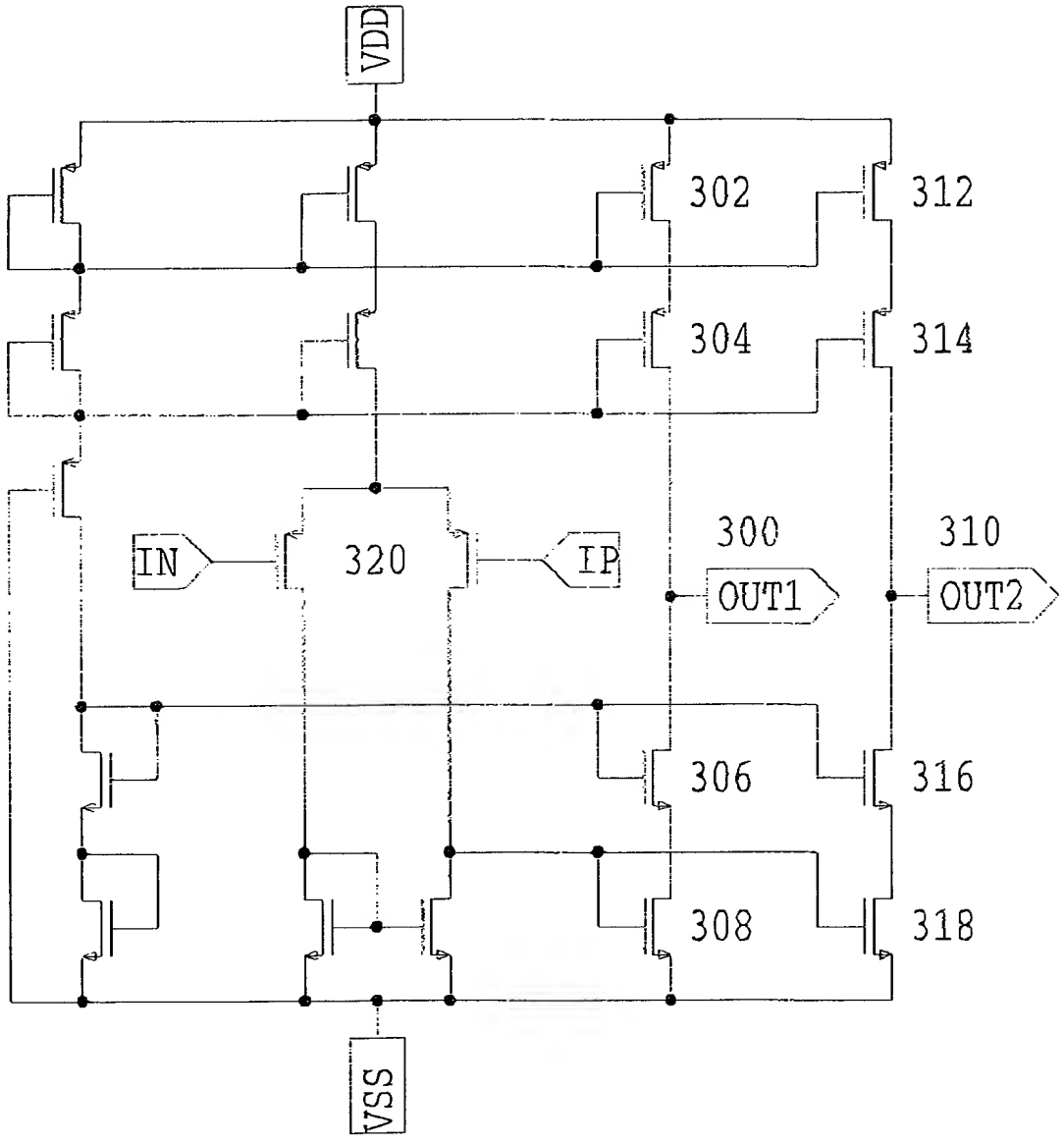
VDD
302
312
304
314
IN
320
IP
300
OUT1
310
OUT2
306
316
308
318
VSS

FIGURE 3

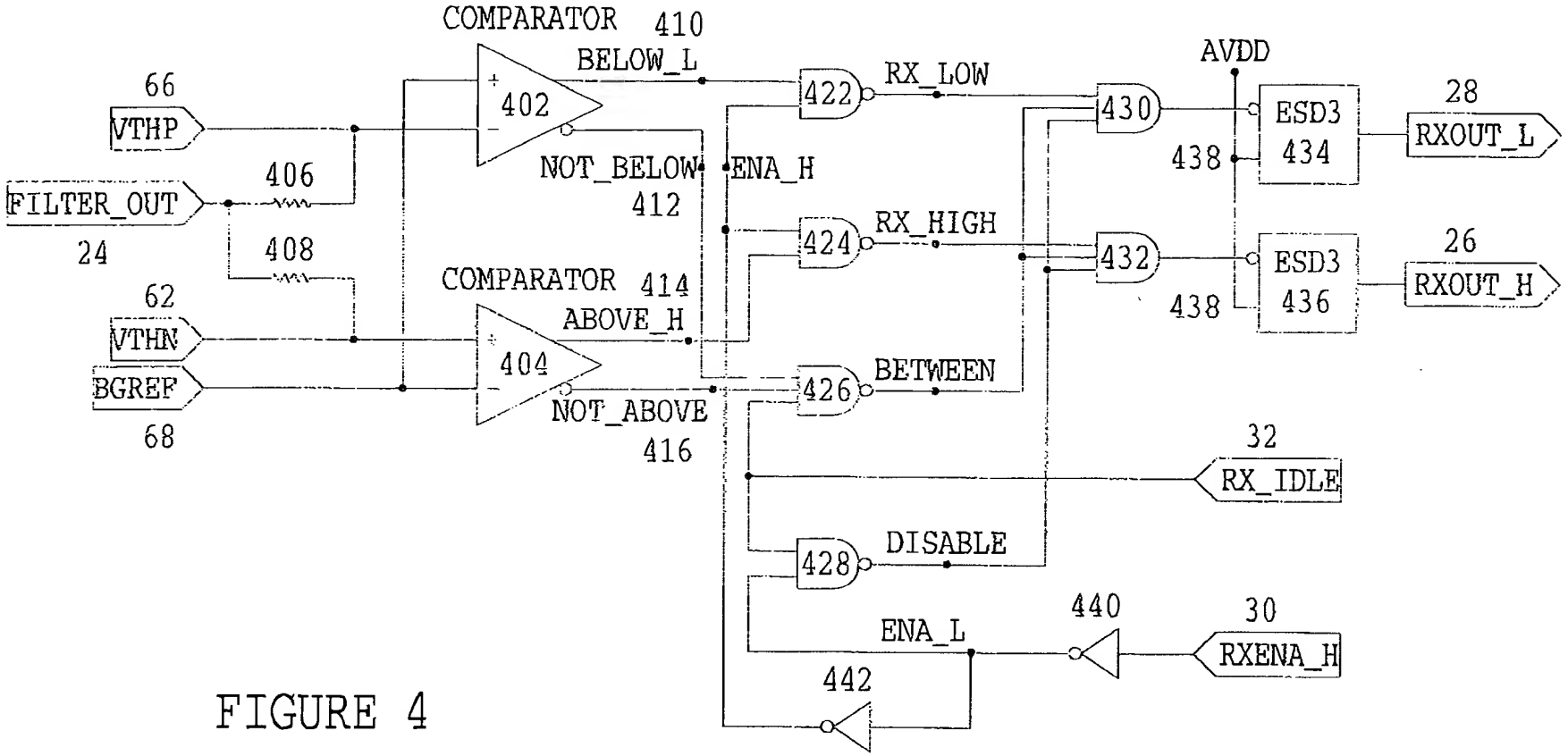

FIGURE 4

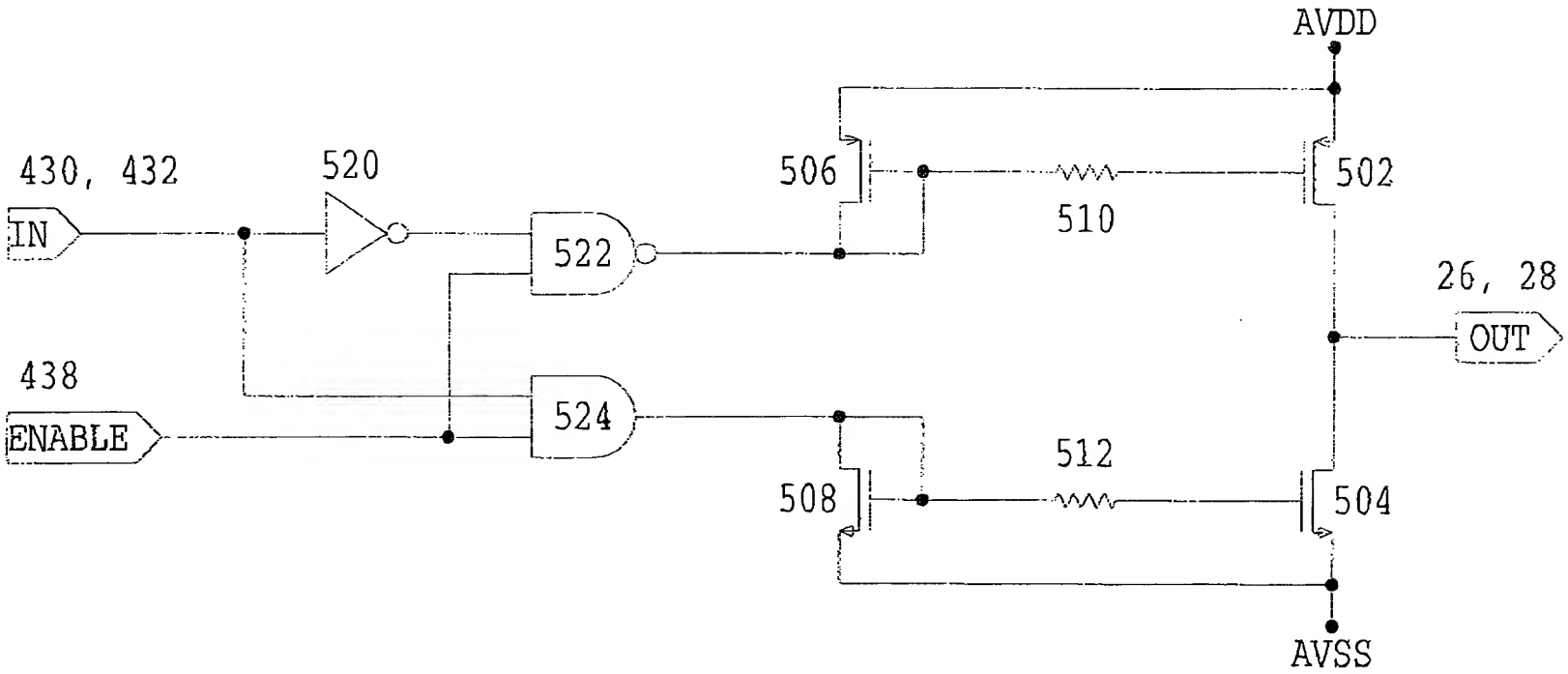

FIGURE 5

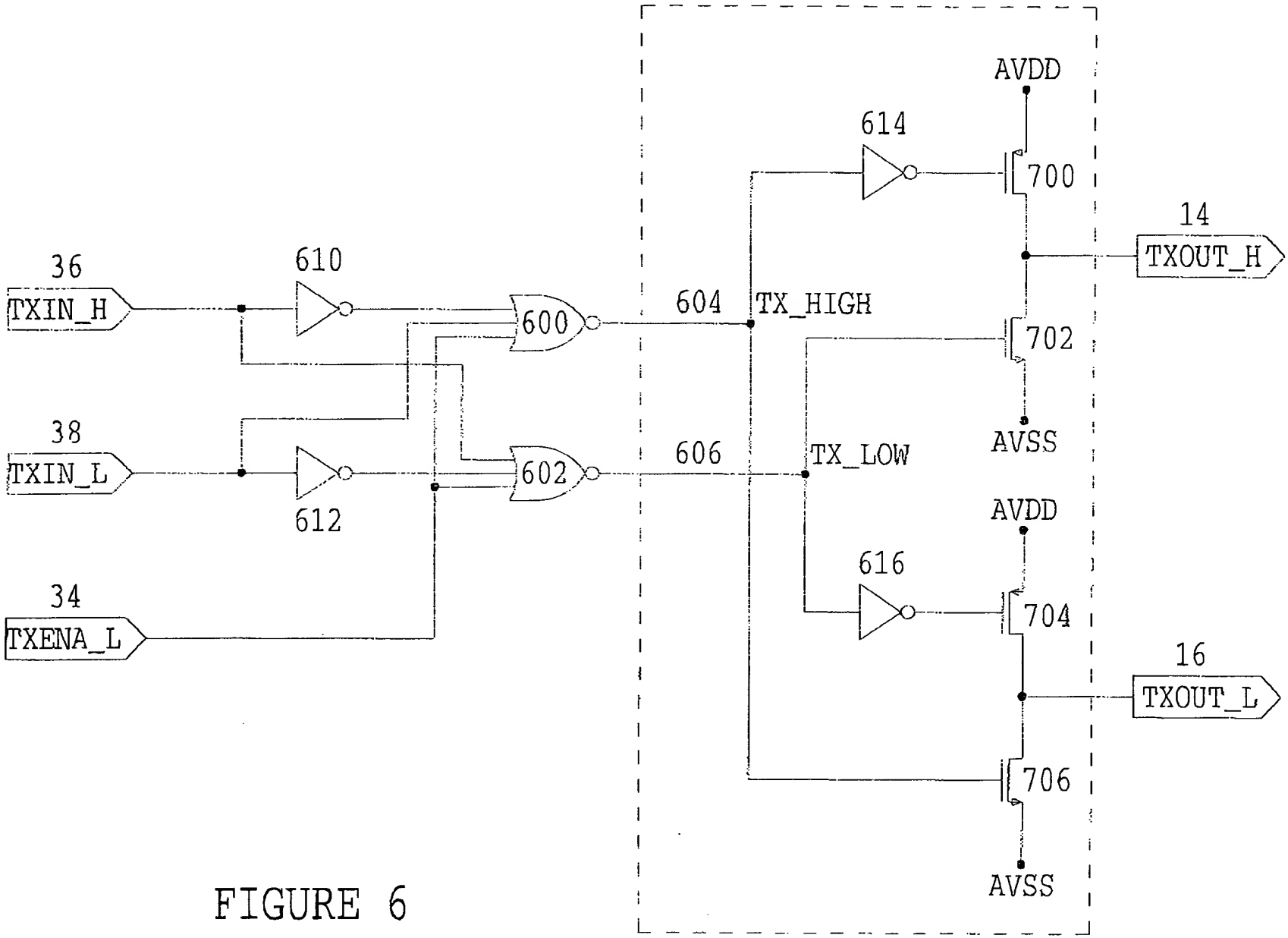

FIGURE 6

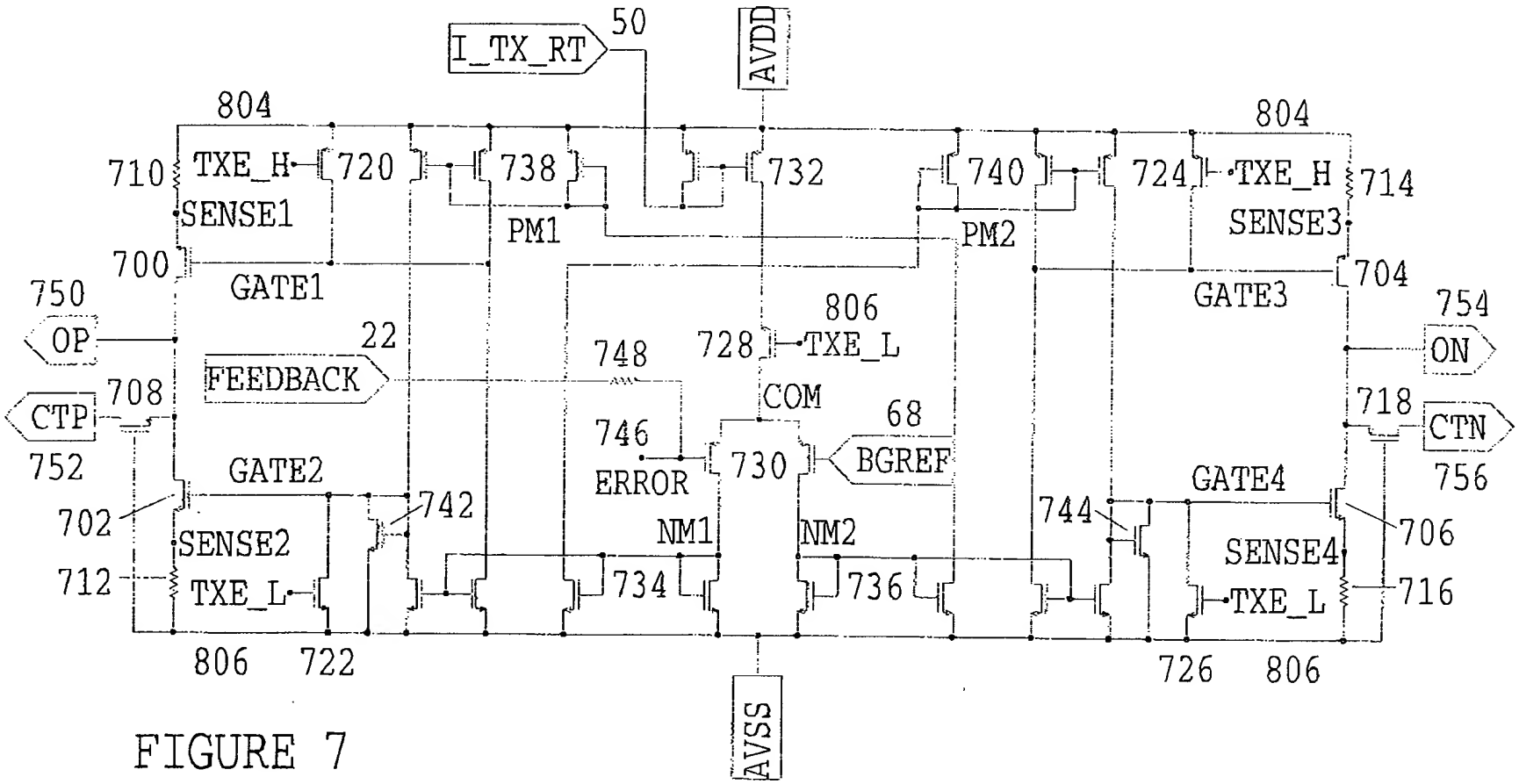
I_TX_RT
50
AVDD
804
804
710
TXE_H
720
738
732
740
724
TXE_H
714
SENSE1
PM1
PM2
SENSE3
700
GATE1
704
GATE3
750
806
754
OP
728
TXE_L
ON
22
748
FEEDBACK
COM
708
718
CTP
746
68
CTN
752
GATE2
730
BGREF
GATE4
756
702
742
ERROR
744
706
NM1
NM2
SENSE2
712
TXE_L
734
736
SENSE4
716
TXE_L
806
722
726
806
AVSS

FIGURE 7

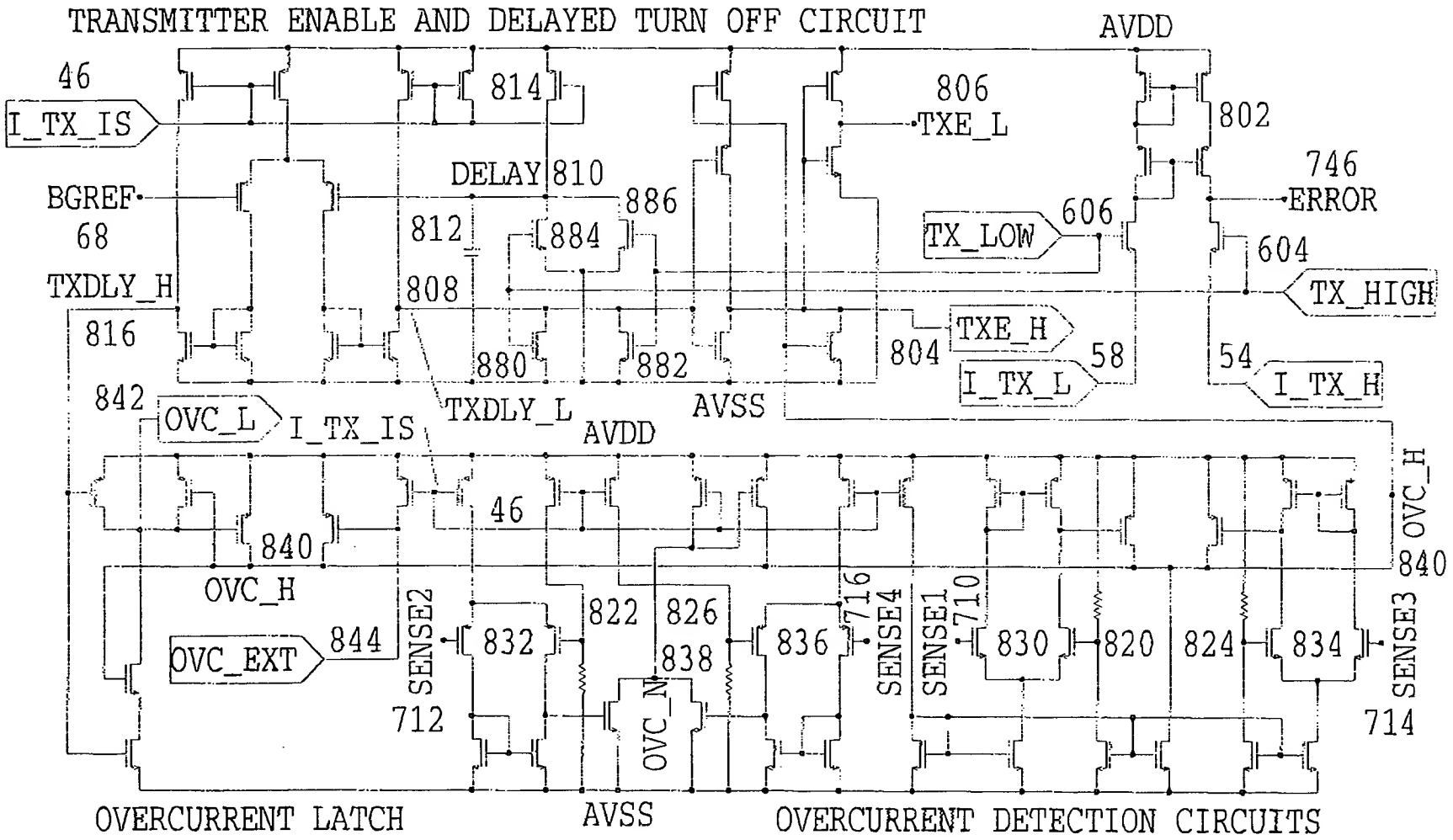

FIGURE 8

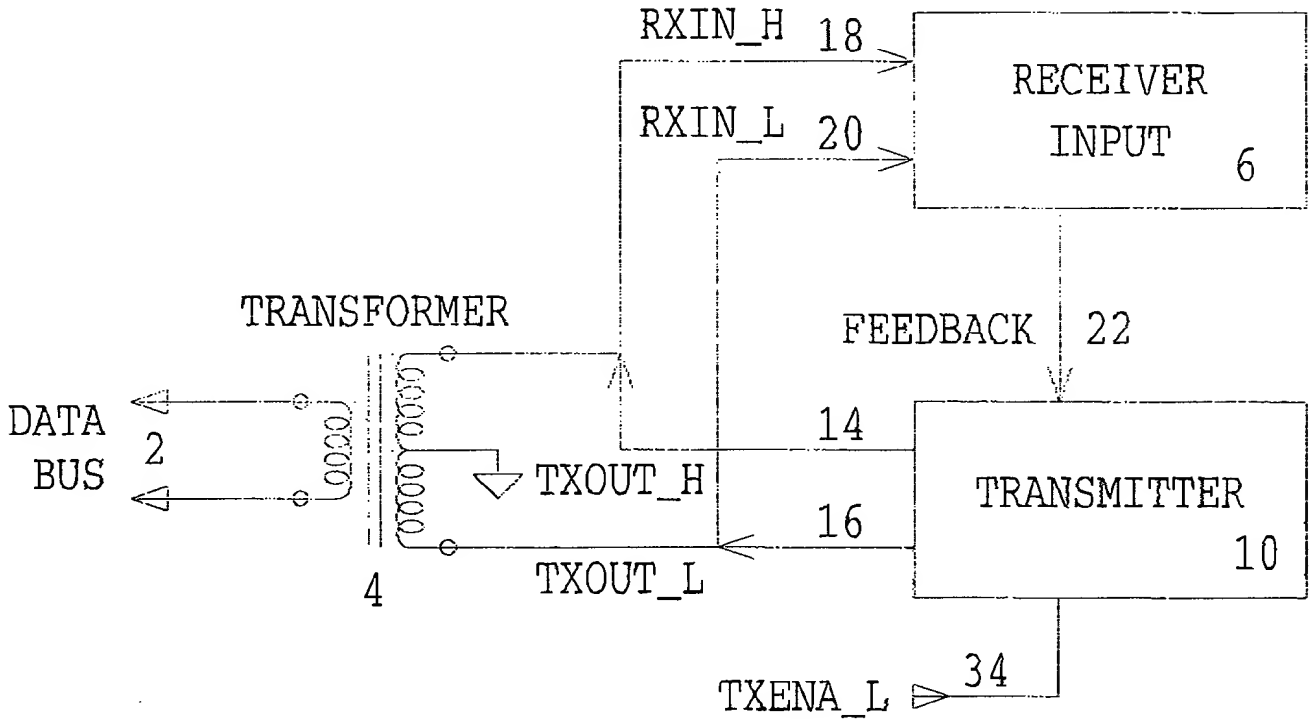

FIGURE 9

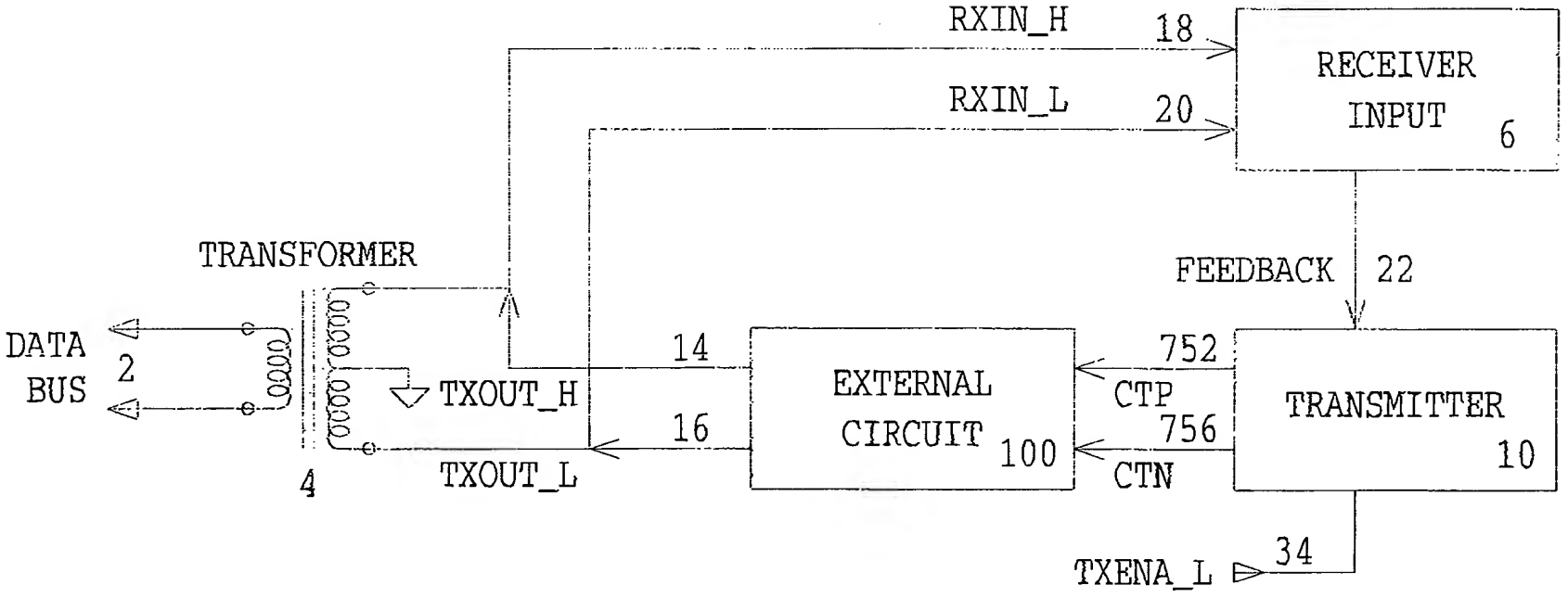
RXIN_H
18
RXIN_L
20
RECEIVER
INPUT
6
TRANSFORMER
FEEDBACK
22
DATA
BUS
2
14
TXOUT_H
16
TXOUT_L
4
EXTERNAL
CIRCUIT
100
752
CTP
756
CTN
TRANSMITTER
10
TXENA_L
34

FIGURE 10

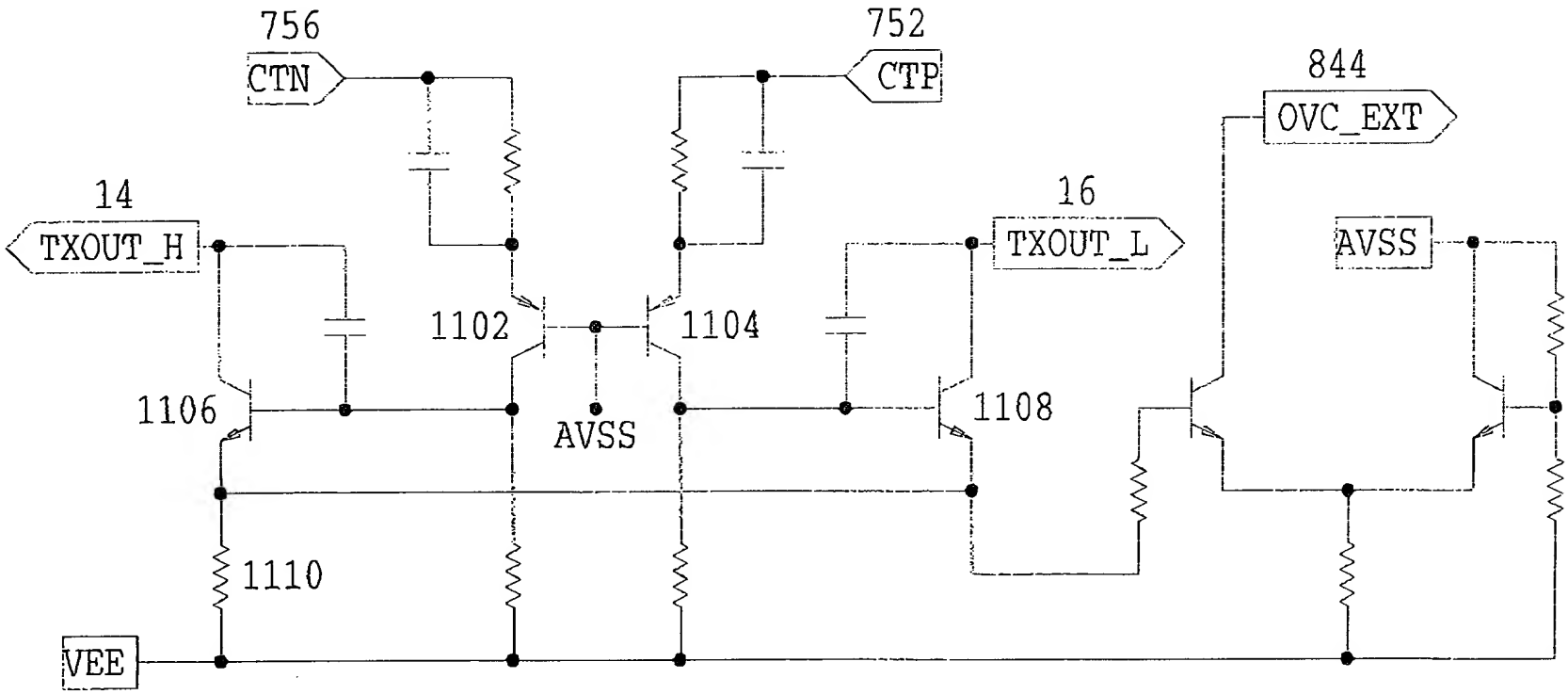
756
CTN
752
CTP
844
OVC_EXT
14
TXOUT_H
16
TXOUT_L
AVSS
1102
1104
1106
1108
AVSS
1110
VEE

FIGURE 11

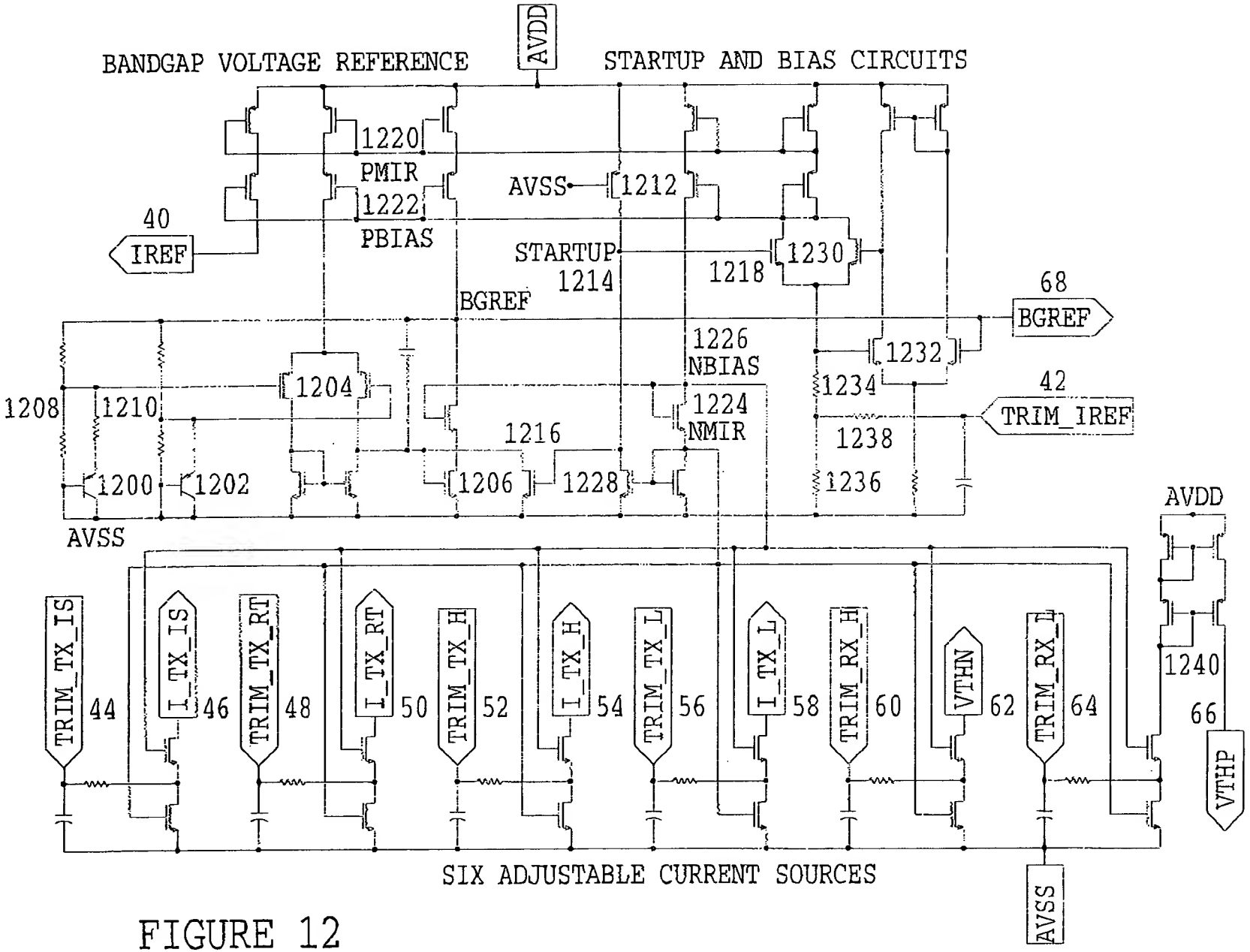

FIGURE 12